



# Call for Papers

## MAIN TOPICS

- Design and synthesis of systems, hardware and embedded software
- IoT, cyber-physical and embedded systems
- Systems-on-a-chip, networks-on-a-chip and systems-in-a-package
- Programmable/re-configurable/adaptable architectures
- Reliable, Secure, Safe and Optimized systems and their design methods
- Formal Methods in System Design
- Specification, modeling, analysis, validation and test for systems, hardware and embedded software
- Design automation at system, register-transfer, logic and physical levels
- Advanced applications of embedded and cyber-physical systems
- Application analysis and parallelization for embedded and high-performance design
- New issues introduced by emerging applications and technologies

## SPECIAL SESSIONS/ORGANIZERS

- **AHSA:** Architectures and Hardware for Security Applications  
*P. Kitsos (TEI of Western Greece, GR), M. Novotny (CTU Prague, CZ)*
- **AAMTM** Applications, Architectures, Methods and Tools for Machine - and Deep Learning  
*M. Shafique (TU Wien, A)*
- **ASAASIT** Architectures and Systems for Automotive, Aeronautic, Space and Intelligent Transportation  
*R. Nouacer (CEA, FR), H. Espinoza (CEA, FR)*
- **ASHWPA** Advanced Systems in Healthcare, Wellness and Personal Assistance  
*F. Loporati (U Pavia, IT), R. Stojanovic (U Montenegro, ME)*
- **HASF** Hardware and Systems for Smart Farming, Food Chain and Sustainable Environments  
*T. Ourphanoudakis (Hellenic Open U, GR), D. Quaglia (U Verona, IT), D. Demarchi (P Torino, IT)*
- **DCPS** Design of Cyber-Physical Systems  
*W. Chang (U York, UK)*
- **DTFT** Dependability, Testing and Fault Tolerance in Digital Systems  
*P. Fiser (CTU Prague, CZ), Z. Kotasek (TU Brno, CZ)*
- **EPDSD** European Projects in Digital System Design  
*F. Loporati (U Pavia, IT), L. Jozwiak (TU/e, NL)*
- **FTET** Future Trends in Emerging Technologies  
*O. Keszocze (FAU Erlangen, DE), P. Fiser (CTU in Prague, CZ)*
- **MCS DIA:** Mixed-Criticality System Design, Implementation and Analysis  
*K. Grüttner (OFFIS, DE), E. Villar (TEISA U Cantabria, ES)*
- **SDCIS:** System Design for Collaborating Intelligent Systems  
*E. Ebeid (U Southern Denmark, DK), R. Jacobsen (Aarhus U, DK), B. Beach (U Southern Denmark, DK)*
- **SPCPS** Security and Privacy of Cyber-Physical Systems  
*S. Dey (IIT Kharagpur, India), S. Park (TU Berlin, D) J. Sepúlveda (Airbus Defence and Space GmbH, D)*

The author's session selection may be changed to create a thematically comprehensive DSD conference program.

Conference Venue

Splendid Hotel La Torre, Palermo, Sicily, Italy

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latorre@latorrehotel.it



# 24<sup>th</sup> Euromicro Conference on Digital System Design

Palermo, Sicily, Italy, Sept. 6<sup>th</sup> – Sept. 8<sup>th</sup>, 2021

## Call for Papers

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### SCOPE

The Euromicro Conference on Digital System Design (DSD) addresses all aspects of (embedded, pervasive and high-performance) digital and mixed HW/SW system engineering, covering the whole design trajectory from specification down to micro-architectures, digital circuits and VLSI implementations. It is a forum for researchers and engineers from academia and industry working on advanced investigations, developments and applications.

It focuses on today's and future challenges of advanced embedded, high-performance and cyber-physical applications; system and processor architectures for embedded and high-performance HW/SW systems; design methodology and design automation for all design levels of embedded, high-performance and cyber-physical systems; modern implementation technologies from full custom in nanometer technology nodes, through FPGAs, to MPSoC infrastructures.

Authors are kindly invited to submit their work according (but not limited) to the main topics of the conference main track. In addition, several Special Sessions (with their own coordinators and subprogram committees) do also welcome contributions in specific themes of particular interest. All papers are reviewed following guidelines, quality requirements and thresholds that are common to all committees.

### SUBMISSION GUIDELINES

Authors are encouraged to submit their manuscripts via EasyChair web service at web page <https://easychair.org/conferences/?conf=dsd2021>. Should an unexpected web access problem be encountered, please contact the Program Chair by email ([dsd2021@easychair.org](mailto:dsd2021@easychair.org)).

Each manuscript should include the complete paper text, all illustrations, and references. The manuscript should conform to the IEEE format: single-spaced, double column, US letter page size, 10-point size Times Roman font, up to 8 pages. In order to conduct a blind review, no indication of the authors' names should appear in the manuscript, references included.

**IEEE Conference Publishing Services (CPS) will publish accepted papers in the conference proceedings and the proceedings will be submitted to the IEEE Xplore Digital library and indexing services.**

**Extended versions of selected best papers will be published in a special issue of the ISI indexed Euromicro/Elsevier journal "Microprocessors and Microsystems: Embedded Hardware Design" (MICPRO) having the 2020 Impact Factor as high as 1.16.**

### IMPORTANT DATES

**Deadline for paper submission: April 1<sup>st</sup>**  
**Notification of acceptance: May 15<sup>th</sup>**  
**Camera ready papers: June 15<sup>th</sup>**

### MORE INFORMATION (WEB PAGES)

- DSD 2021:
- Euromicro: [www.euromicro.org](http://www.euromicro.org)