



DSD 2020

23rd Euromicro Conference on Digital System Design

Grand Hotel Bernardin, Portorož, Slovenia, 26th – 28th August, 2020

Call for Papers (Extended deadline due to Covid-19)

MAIN TOPICS

- Design and synthesis of systems, hardware and embedded software
- IoT, cyber-physical, embedded and high-performance systems
- Systems-on-a-chip, networks-on-a-chip and systems-in-a-package
- Autonomous, self-aware, re-configurable and adaptable systems, and their design
- Reliable, Secure, Safe and Optimized to multiple objectives systems, and their design
- Formal Methods in System Design
- Specification, modeling, analysis, validation and test for systems, hardware and embedded software
- Design automation at system, register-transfer, logic and physical levels
- Advanced applications of cyber-physical, embedded and high-performance systems
- Application analysis and parallelization for embedded and high-performance design
- New issues introduced by emerging applications and technologies, and their solutions

SPECIAL SESSIONS/ORGANIZERS

- **AHSA:** Architectures and Hardware for Security Applications, P. Kitsos (University of Peloponnese, GR), M. Novotny (CTU Prague, CZ)
- **ASAASIT:** Architectures and Systems for Automotive, Aeronautic, Space and Intelligent Transportation, R. Nouacer (CEA, FR), H. Espinoza (CEA, FR)
- **ASHWPA:** Advanced Systems in Healthcare, Wellness and Personal Assistance | F. Loporati (U Pavia, IT), R. Stojanovic (U Montenegro, ME)
- **DCPS:** Design of Cyber-Physical Systems, W. Chang (U York, UK), D. Mueller-Gritschneider (TU Munich, DE)
- **DTFT:** Dependability, Testing and Fault Tolerance in Digital Systems | P. Fiser (CTU Prague, CZ), Z. Kotasek (TU Brno, CZ)
- **EDLAI:** Embedded Deep Learning and AI: Architectures, Design Methods, Tools, and Applications | M. Shafique (TU Wien, A)
- **EPDSD:** European Projects in Digital System Design | F. Loporati (U Pavia, IT), L. Jozwiak (TU/e, NL)
- **ESSAFE:** Electronic systems for smart agriculture, food chain and sustainable environments D. Quaglia (U Verona, IT), D. Demarchi (P Torino, IT)
- **FTET:** Future Trends in Emerging Technologies | O. Keszocze (FAU Erlangen, DE), P. Fiser (CTU in Prague, CZ)
- **HASF:** Hardware and System Architectures for Smart Farming, T. Orphanoudakis (Hellenic Open University, GR)
- **MCSDIA:** Mixed-Criticality System Design, Implementation and Analysis | K. Grüttner (OFFIS, DE), M. Azkarate-Askatsua (IK4-IKERLAN, ES)
- **SDCIS:** System Design for Collaborating Intelligent Systems | | R. H. Jacobsen (Aarhus U, DK)
- **SPCPS:** Security and Privacy of Cyber-Physical Systems | | S. Dey (IIT Kharagpur, India), S. Park (TU Berlin, D) J. Sepúlveda (Airbus Defence and Space GmbH, D)

Conference Venue

Grand Hotel Bernardin

Hoteli Bernardin d.d., St. Bernardin Resort, Obala 2, SI - 6320 Portorož, Slovenia

<https://www.hoteli-bernardin.si/en/>, booking@h-bernardin.si, +386 5 695 1000





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SCOPE

The Euromicro Conference on Digital System Design (DSD) addresses all aspects of (embedded, pervasive and high-performance) digital and mixed HW/SW system engineering, covering the whole design trajectory from specification down to micro-architectures, digital circuits and VLSI implementations. It is a forum for researchers and engineers from academia and industry working on advanced investigations, developments and applications.

It focuses on today's and future challenges of advanced embedded, high-performance and cyber-physical applications; system and processor architectures for embedded and high-performance HW/SW systems; design methodology and design automation for all design levels of embedded, high-performance and cyber-physical systems; modern implementation technologies from full custom in nanometer technology nodes, through FPGAs, to MPSoC infrastructures.

Authors are kindly invited to submit their work according (but not limited) to the main topics of the conference main track. In addition, several Special Sessions (with their own coordinators and subprogram committees) do also welcome contributions in specific themes of particular interest. All papers are reviewed following guidelines, quality requirements and thresholds that are common to all topics and sessions. Please find the Calls for Papers for Special Sessions and more details at the conference webpage: <https://dsd-sea2020.um.si/>

SUBMISSION GUIDELINES

Authors are encouraged to submit their manuscripts via EasyChair web service at web page <https://easychair.org/conferences/?conf=dsd2020>. Should an unexpected web access problem be encountered, please contact the Program Chair by email (Andrej.Zemva@fe.uni-lj.si). Each manuscript should include the complete paper text, all illustrations, and references. The manuscript should conform to the IEEE format: single-spaced, double column, US letter page size, 10-point size Times Roman font, up to 8 pages. In order to conduct a blind review, no indication of the authors' names should appear in the manuscript, references included.



CPS, Conference Publishing Services, publishes the (ISI indexed) DSD Proceedings, available worldwide through the IEEE Xplore Digital Library. Extended versions of selected best papers will be published in a special issue of the ISI indexed "Microprocessors and Microsystems: Embedded Hardware Design" Elsevier journal.

IMPORTANT DATES

Deadline for paper submission: April 24th (Ext.)

Notification of acceptance: June 6th

Camera ready papers: June 20th

MORE INFORMATION

• DSD 2020: <https://dsd-sea2020.um.si/>

• Euromicro: www.euromicro.org